

Notice of References Cited	Application/Control No. 10/635,373	Applicant(s)/Patent Under Reexamination ADKISSON ET AL.	
	Examiner Cynthia Britt	Art Unit 2138	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,881,224 A	03-1999	Ranson et al.	714/47
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	High-Speed and Low Swing On-Chip Bus Interface Using Threshold Voltage Swing Driver and Dual Sense Amplifier by Yang et al. Solid-State Circuits Conference, 2000. ESSCIRC '00. Proceedings of the 26th European 19-21 Sept. 2000 Page(s):105 - 108
	V	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.